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- (71) Applicant (for all designated States except US): THOM-SON LICENSING S.A. [FR/FR]; 46, Quai A. Le Gallo, F-92100 Boulogne-Billancourt (FR).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): LENDARO, Jeffery, Basil [US/US]; 1107 East Logan Street, Noblesville, Indiana 46060 (US).
- (74) Agents: TRIPOLI, Joseph, S. et al.; Two Independence Way, Suite #200, Princeton, New Jersey 08540 (US).

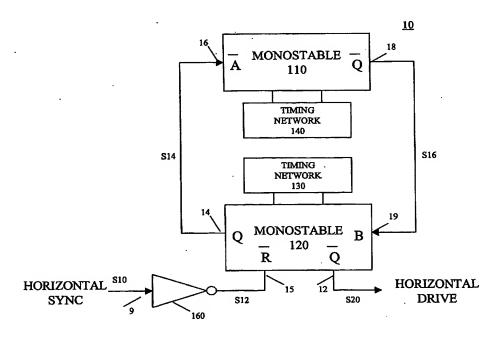
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(54) Title: TIMING GENERATOR



(57) Abstract: A timing generator and method of developing drive signals (S10) which, when input sync signals (S10) are present, delivers the leading and trailing edges of the input pulses (TS) directly to a drive output (TD) and which generates a free-running signal (S20') when input sync pulses are not present.